EAST - [Untitled1:1]

File View Edit Tools Window Help

- Drafts
- Pending
- Active
  - L1: (118483) disk adj1 drive
  - L2: (33341) clock near5 delay
  - L3: (723546) recording
  - L4: (745) pre-compensate or pre-compensation
  - L5: (755) precompensate or precompensation
  - L6: (1391) 4 or 5
  - L7: (0) 1 same 2 same 3 same 6
  - L8: (65) 1 and 2 and 3 and 6
- Failed
- Saved

DB: US-PGPUB; USPAT; EPO; JPO; IBM_TDB

Default operator: OR

1 and 2 and 3 and 6

#	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current OR	Retrieval	Inventor	S	C	P		3	
1	✓		US 20030189778	20031009	14	Disk drive writer waveform induced precom	360/29	360/46; 360/66		Elliott, Carl F. et al.						
2	✓		US 20020176299	20021128		High-speed zero phase restart of a multiphase	365/221	365/233		Ruegg, Michael A. et al.						
3	✓		US 20020176194	20021128		High-speed programmable synchronous counter for	360/51			Ruegg, Michael A. et al.						
4	✓		US 20020176188	20021128		Offset cancellation of charge pump based phase	360/46			Ruegg, Michael A. et al.						
5	✓		US 20020176186	20021128		Efficient analog front end for a read/write ch	360/32	360/51		Cyrusian, Sasan et al.						
6	✓		US 20020175724	20021128		Method and system for managing a pulse width	327/172			Cyrusian, Sasan et al.						
7	✓		US 20020159176	20021031		Look ahead write precompensation	360/46	360/51		Elliott, Carl F. et al.						
8	✓		US 20020154430	20021024		Read/write channel	360/25			Rae, James W. et al.						
9	✓		US 20020126604	20020912		Write compensation for data storage and commun	369/47.53	369/59.11		Powelson, Judith C. et al.						
10	✓		US 20020015247	20020207		HIGH RESOLUTION WIDE RANGE WRITE PRECOMPENSA	360/45	360/68		ROSKY, DAVID S. et al.						
11	✓		US 6785071 B2	20040831		Disk drive writer waveform induced precom	360/29	360/46; 360/66		Elliott; Carl F. et al.						
12	✓		US 6738205 B1	20040518		Self-writing of servo patterns in disk drives	360/17	360/75		Moran; Patrick et al.						
13	✓		US 6700722 B2	20040302		High-speed zero phase restart of a multiphase	360/51	327/152; 331/2;		Ruegg; Michael A. et al.						
14	✓		US 6690525 B2	20040210		High-speed programmable synchronous counter for	360/51	331/25; 375/376;		Ruegg; Michael A. et al.						
15	✓		US 6662303 B1	20031209		Write precompensation circuit and read channe	713/400	711/167; 713/500;		Toosky; Zabih et al.						
16	✓		US 6661590 B2	20031209		Efficient analog front end for a read/write ch	360/32	360/51; 360/66		Cyrusian; Sasan et al.						
17	✓		US 6653879 B2	20031125		Method and system for managing a pulse width	327/172	327/173; 327/174		Cyrusian; Sasan et al.						

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	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current XR	Retrieval	Inventor	S	C	P			
18	☑	☐	US 6621648 B2	20030916		Look ahead write precompensation	360/45	360/46; 360/51;		Elliott; Carl F. et al.	☐	☐	☐	☐	☐	☐
19	☑	☐	US 6594094 B2	20030715		Read/write channel	360/25	360/46; 360/51;		Rae; James W. et al.	☐	☐	☐	☐	☐	☐
20	☑	☐	US 6438703 B1	20020820		Method and system for selectively varying sig	713/401	713/503		Berning; Lynn Charles et al.	☐	☐	☐	☐	☐	☐
21	☑	☐	US 6404572 B1	20020611		Circuit and method for generating a write prec	360/45	360/51; 360/76		Hong; Ghy-Boong	☐	☐	☐	☐	☐	☐
22	☑	☐	US 6337778 B1	20020108		Disk drive employing vector addition of prim	360/51	360/46; 360/68		Gagne; Jean-Sebastien	☐	☐	☐	☐	☐	☐
23	☑	☐	US 6243031 B1	20010605		Selectable delay circuit having immunity	341/68	327/157		Jusuf; Gani et al.	☐	☐	☐	☐	☐	☐
24	☑	☐	US 6219192 B1	20010417		Method and apparatus for the compensation of	360/25	360/46; 360/53;		Gopalaswamy; Srinivasan et al.	☐	☐	☐	☐	☐	☐
25	☑	☐	US 6208184 B1	20010327		Method and device for delaying selected trans	327/161	327/263		Demicheli; Marco et al.	☐	☐	☐	☐	☐	☐
26	☑	☐	US 6191906 B1	20010220		Method for estimating sampling phase from syn	360/51	360/46		Buch; Bruce D.	☐	☐	☐	☐	☐	☐
27	☑	☐	US 6133861 A	20001017		Selectable delay circuit having immunity	341/68	327/157		Jusuf; Gani et al.	☐	☐	☐	☐	☐	☐
28	☑	☐	US 6108153 A	20000822		Servo demodulator and method for synchronous	360/51	360/77.08		Glover; Kerry C.	☐	☐	☐	☐	☐	☐
29	☑	☐	US 6043944 A	20000328		Method and apparatus for failsafing and exte	360/51			Yamasaki; Richard G. et al.	☐	☐	☐	☐	☐	☐
30	☑	☐	US 6009534 A	19991228		Fractional phase interpolation of ring o	713/503	331/107R		Chiu; Kar-Shing et al.	☐	☐	☐	☐	☐	☐
31	☑	☐	US 5986830 A	19991116		Read/write channel write precompensation s	360/45	360/51		Hein; Jerrell P.	☐	☐	☐	☐	☐	☐
32	☑	☐	US 5808822 A	19980915		Apparatus and method for correcting a read s	360/65	360/51		Kurachi; Koji et al.	☐	☐	☐	☐	☐	☐
33	☑	☐	US 5734680 A	19980331		Analog implementation of a partial response m	375/263	375/262; 375/341;		Moore; Charles E. et al.	☐	☐	☐	☐	☐	☐
34	☑	☐	US 5598364 A	19970128		All-MOS precision differential delay line	365/73	360/51; 365/189.05		McCall; Kevin J. et al.	☐	☐	☐	☐	☐	☐
35	☑	☐	US 5592515 A	19970107		Fully digital data separator and frequency	375/340	327/156; 327/160;		Saban; Rami et al.	☐	☐	☐	☐	☐	☐
36	☑	☐	US 5583705 A	19961210		Write precompensation optimization in a PRML	360/45	360/46		Ziperovich; Pablo A. et al.	☐	☐	☐	☐	☐	☐
37	☑	☐	US 5559645 A	19960924		Disk recording apparatus with adaptive	360/51	360/53		Miyazawa; Shyoichi et al.	☐	☐	☐	☐	☐	☐
38	☑	☐	US 5553100 A	19960903		Fully digital data separator and frequency	375/340	327/156; 327/160;		Saban; Rami et al.	☐	☐	☐	☐	☐	☐
39	☑	☐	US 5552942 A	19960903		Zero phase start optimization using mean	360/51	375/369; 375/376		Ziperovich; Pablo A. et al.	☐	☐	☐	☐	☐	☐
40	☑	☐	US 5543975 A	19960806		Removal of precompensation in a wr	360/45	360/46		Curran; Daniel J.	☐	☐	☐	☐	☐	☐
41	☑	☐	US 5517146 A	19960514		Write precompensation circuit for compensatin	327/276	327/261; 360/46		Yamasaki; Richard	☐	☐	☐	☐	☐	☐
42	☑	☐	US 5493454	19960220		Write precompensation	360/45	360/46;		Ziperovich; Pablo	☐	☐	☐	☐	☐	☐

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U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current XR	Retrieval	Inventor	S	C	P		?	
42	☑	☐	US 5493454 A	19960220		Write precompensation optimization in a PRML	360/45	360/46; 360/53		Ziperovich; Pablo A. et al.	☐	☐	☐	☐	☐	☐
43	☑	☐	US 5459757 A	19951017		Timing and gain control circuit for a PRML read	375/376	360/46; 360/51;		Minuhin; Vadim B. et al.	☐	☐	☐	☐	☐	☐
44	☑	☐	US 5422760 A	19950606		Disk drive method using zoned data recording an	360/46	360/51; 360/65		Abbott; William L. et al.	☐	☐	☐	☐	☐	☐
45	☑	☐	US 5402274 A	19950328		Data separator and signal processing circu	360/51	360/53		Miyazawa; Shyoichi et al.	☐	☐	☐	☐	☐	☐
46	☑	☐	US 5381359 A	19950110		Adaptation and training of digital finite impul	708/322	360/40		Abbott; William L. et al.	☐	☐	☐	☐	☐	☐
47	☑	☐	US 5375145 A	19941220		Multi-mode gain control loop for PRML class IV	375/345	375/290; 455/234.1		Abbott; William L. et al.	☐	☐	☐	☐	☐	☐
48	☑	☐	US 5369376 A	19941129		Programmable phase locked loop circuit and	331/8	331/16; 331/17;		Leblebicioglu; Hakan	☐	☐	☐	☐	☐	☐
49	☑	☐	US 5345342 A	19940906		Disk drive using PRML synchronous sampling da	360/48	360/77.08		Abbott; William L. et al.	☐	☐	☐	☐	☐	☐
50	☑	☐	US 5341387 A	19940823		Viterbi detector having adjustable detection th	714/788	375/341; 375/348;		Nguyen; Hung C.	☐	☐	☐	☐	☐	☐
51	☑	☐	US 5341249 A	19940823		Disk drive using PRML class IV sampling data	360/46	360/39; 360/51;		Abbott; William L. et al.	☐	☐	☐	☐	☐	☐
52	☑	☐	US 5331479 A	19940719		Capacitor assisted low voltage write circuit	360/68	360/46; 360/66;		Madsen; Timothy A.	☐	☐	☐	☐	☐	☐
53	☑	☐	US 5321559 A	19940614		Asynchronous peak detection of informatio	360/46	360/51; 360/65		Nguyen; Hung C. et al.	☐	☐	☐	☐	☐	☐
54	☑	☐	US 5278702 A	19940111		Data synchronizer with symmetric window genera	360/51	360/46; 375/376		Wilson; Ronald E. et al.	☐	☐	☐	☐	☐	☐
55	☑	☐	US 5258933 A	19931102		Timing control for PRML class IV sampling data	708/3	360/46; 360/51;		Johnson; Kenneth E. et al.	☐	☐	☐	☐	☐	☐
56	☑	☐	US 5231545 A	19930727		Fault tolerant RLL data sector address mark dec	360/49	341/59; 360/40		Gold; Clifford M.	☐	☐	☐	☐	☐	☐
57	☑	☐	US 5212601 A	19930518		Disk drive data synchronizer with windo	360/51	360/44		Wilson; Ronald E.	☐	☐	☐	☐	☐	☐
58	☑	☐	US 5187615 A	19930216		Data separator and signal processing circu	360/46	327/160; 360/51		Miyazawa; Shyoichi et al.	☐	☐	☐	☐	☐	☐
59	☑	☐	US 5025327 A	19910618		Magnetic disk write precompensation method	360/45	360/27		Stamness; Jesse I. et al.	☐	☐	☐	☐	☐	☐
60	☑	☐	US 4989219 A	19910129		Midlevel carrier modulation and demodula	375/286	329/363; 332/115;		Gerdes; Richard C. et al.	☐	☐	☐	☐	☐	☐
61	☑	☐	US 4964107 A	19901016		Relative timing precompensation of high	368/120	360/51		Galbraith; Richard L. et al.	☐	☐	☐	☐	☐	☐
62	☑	☐	US 4878028 A	19891031		Technique for generating precompensat	327/278	327/101; 327/262;		Wang; Yun-Che et al.	☐	☐	☐	☐	☐	☐
63	☑	☐	US 4809088 A	19890228		Integrated system for implementation of read	360/31	360/45; 360/51		Lofgren; Karl M. J. et al.	☐	☐	☐	☐	☐	☐
64	☑	☐	US 4544964 A	19851001		Strobe for read/write chain	360/51			Burns; Kenneth R. et al.	☐	☐	☐	☐	☐	☐
65	☑	☐	US 4281356 A	19810728		Magnetic disk memory	360/45	360/51		Sousa; Frank E.	☐	☐	☐	☐	☐	☐

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